10660310_CLSTITLES1.txt

Titles of Most Frequently Occurring Classifications of Patents Returned From A Search of 10660310 on March 07, 2006

```
711/118
                  (4 OR, 3 XR)
        Class
                 711:
                        ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                         SYSTEMS: MEMORY
        711/100
                       STORAGE ACCESSING AND CONTROL
        711/117
                       .Hierarchical memories
        711/118
                       ..Caching
                  (3 OR, 4 XR)
  711/144
                 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
        Class
                         SYSTEMS: MEMORY
                       STORAGE ACCESSING AND CONTROL
        711/100
        711/117
                       .Hierarchical memories
        711/118
                       ...Caching
        711/141
                       ...Coherency
        711/144
                       ....Cache status data bit
                  (1 OR, 5 XR)
  711/146
        Class
                 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                         SYSTEMS: MEMORY
                       STORAGE ACCESSING AND CONTROL
        711/100
        711/117
                       .Hierarchical memories
        711/118
                       ..Caching
        711/141
                       ... Coherency
        711/146
                       ....Snooping
   711/3
                  (2 OR, 4 XR)
6
                 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
        Class
                         SYSTEMS: MEMORY
                       ADDRESSING COMBINED WITH SPECIFIC MEMORY
        711/1
                            CONFIGURATION OR SYSTEM
        711/3
                       .Addressing cache memories
                  (1 OR, 4 XR)
5
   711/145
        class
                 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                         SYSTEMS: MEMORY
        711/100
                       STORAGE ACCESSING AND CONTROL
        711/117
                       .Hierarchical memories
        711/118
                       .. Caching
        711/141
                       ...Coherency
        711/145
                       ....Access control bit
  711/119
                  (0 \text{ OR}, 4 \text{ XR})
                 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
        Class
                         SYSTEMS: MEMORY
                       STORAGE ACCESSING AND CONTROL
        711/100
        711/117
711/118
                       .Hierarchical memories
                       ..Caching
        711/119
                       ...Multiple caches
   711/133
                  (1 \text{ OR}, 3 \text{ XR})
        Class
                 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                         SYSTEMS: MEMORY
        711/100
                       STORAGE ACCESSING AND CONTROL
        711/117
                       .Hierarchical memories
        711/118
                       ..Caching
        711/133
                       ...Entry replacement strategy
3
  710/22
                  (1 \text{ OR}, 2 \text{ XR})
                 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
        Class
                                       Page 1
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10660310_CLSTITLES1.txt PROCESSING SYSTEMS: INPUT/OUTPUT 710/1 INPUT/OUTPUT DATA PROCESSING 710/22 .Direct Memory Accessing (DMA) (2 OR, 1 XR) 711/130 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING Class SYSTEMS: MEMORY STORAGE ACCESSING AND CONTROL 711/100 711/117 .Hierarchical memories 711/118 ...Caching ...Shared cache 711/130 (2 OR, 1 XR) 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING 711/135 Class SYSTEMS: MEMORY 711/100 STORAGE ACCESSING AND CONTROL 711/117 .Hierarchical memories 711/118 ..Caching 711/133 ...Entry replacement strategyCache flushing 711/135 (1 OR, 2 XR) 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING 711/138 Class SYSTEMS: MEMORY STORAGE ACCESSING AND CONTROL 711/100 711/117 .Hierarchical memories 711/118 ..Caching 711/138 ...Cache bypassing (1 OR, 2 XR) 711/143 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING Class SYSTEMS: MEMORY STORAGE ACCESSING AND CONTROL 711/100 711/117 .Hierarchical memories 711/118 ..Caching 711/141 ...CoherencyWrite-back 711/143 (1 OR, 2 XR)711/147 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING Class SYSTEMS: MEMORY 711/100 STORAGE ACCESSING AND CONTROL 711/147 .Shared memory area (1 OR, 2 XR) 711/207 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING class SYSTEMS: MEMORY 711/200 ADDRESS FORMATION .Address mapping (e.g., conversion, translation) 711/202 .. Virtual addressing 711/203 ...Translation tables (e.g., segment and page 711/206 table or map) 711/207Directory tables (e.g., DLAT, TLB) 712/23 (0 OR, 3 XR)ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: PROCESSING ARCHITECTURES AND INSTRUCTION Class PROCESSING PROCESSING ARCHITECTURE 712/1 712/23 .Superscalar

709/213

(0 OR, 2 XR)

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10660310_CLSTITLES1.txt
               709 :
      class
                      ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                       SYSTEMS: MULTIPLE COMPUTER OR PROCESS
                                                                   COORDINATING
      709/213
                     MULTICOMPUTER DATA TRANSFERRING VIA SHARED
710/105
                (0 \text{ OR}, 2 \text{ XR})
               710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
      Class
                       PROCESSING SYSTEMS: INPUT/OUTPUT
      710/100
                     INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                          TRANSACTION PROCESSING)
                     .Protocol
      710/105
710/108
                (1 OR, 1 XR)
               710 :
                      ELECTRICAL COMPUTERS AND DIGITAL DATA
      class
                       PROCESSING SYSTEMS: INPUT/OUTPUT
      710/100
                     INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                          TRANSACTION PROCESSING)
      710/107
                     .Bus access regulation
      710/108
                     ..Bus locking
710/52
                (1 OR, 1 XR)
      class
               710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
                     PROCESSING SYSTEMS: INPUT/OUTPUT INPUT/OUTPUT DATA PROCESSING
      710/1
      710/52
                     .Input/Output data buffering
711/100
                (1 OR, 1 XR)
      Class
               711 :
                      ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                       SYSTEMS: MEMORY
      711/100
                     STORAGE ACCESSING AND CONTROL
 711/113
                (1 \text{ OR}, 1 \text{ XR})
               711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
      Class
                       SYSTEMS: MEMORY
                     STORAGE ACCESSING AND CONTROL
      711/100
      711/101
                     .Specific memory composition
                     ..Accessing dynamic storage device
      711/111
                     ...Direct access storage device (DASD)
      711/112
      711/113
                     ....Caching
                (1 OR, 1 XR)
711/121
               711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
      Class
                       SYSTEMS: MEMORY
                     STORAGE ACCESSING AND CONTROL
      711/100
      711/117
                     .Hierarchical memories
      711/118
                     ..Caching
      711/119
                     ...Multiple caches
      711/121
                     ....Private caches
               (1 OR, 1 XR)
711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
711/122
      class
                       SYSTEMS: MEMORY
      711/100
                     STORAGE ACCESSING AND CONTROL
      711/117
                     .Hierarchical memories
      711/118
                     ..Caching
      711/119
                     ...Multiple caches
                     ....Hierarchical caches
      711/122
               (0 OR, 2 XR)
711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
711/125
      Class
                       SYSTEMS: MEMORY
      711/100
                     STORAGE ACCESSING AND CONTROL
      711/117
                     .Hierarchical memories
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Page 3

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10660310_CLSTITLES1.txt
        711/118
                        ...Caching
        711/125
                        ...Instruction data cache
  711/139
                  (0 \text{ OR}, 2 \text{ XR})
                 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
        Class
                          SYSTEMS: MEMORY
        711/100
                        STORAGE ACCESSING AND CONTROL
        711/117
                        .Hierarchical memories
        711/118
                        ..Caching
        711/138
                        ...Cache bypassing
                        ....No-cache flags
        711/139
   711/140
                  (0 OR, 2 XR)
                 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
        class
                          SYSTEMS: MEMORY
        711/100
                        STORAGE ACCESSING AND CONTROL
        711/117
                        .Hierarchical memories
        711/118
                        ..Caching
        711/140
                        ...Cache pipelining
                  (0 OR, 2 XR)
2
  711/156
        Class
                 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                          SYSTEMS: MEMORY
                        STORAGE ACCESSING AND CONTROL .Control technique
        711/100
        711/154
        711/156
                        ..Status storage
                  (0 OR, 2 XR)
  711/159
                 711:
                         ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
        Class
                          SYSTEMS: MEMORY
                        STORAGE ACCESSING AND CONTROL .Control technique
        711/100
        711/154
711/159
                        .. Entry replacement strategy
  711/165
                  (0 \text{ OR}, 2 \text{ XR})
                 711 :
                         ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
        Class
                          SYSTEMS: MEMORY
        711/100
711/154
                        STORAGE ACCESSING AND CONTROL
                        .Control technique
..Internal relocation
        711/165
  711/168
                  (2 OR, 0 XR)
        Class
                 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                          SYSTEMS: MEMORY
        711/100
                        STORAGE ACCESSING AND CONTROL
        711/167
                        .Access timing
        711/168
                        ..Concurrent accessing
  711/169
                  (0 OR, 2 XR)
        Class
                 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                          SYSTEMS: MEMORY
        711/100
                        STORAGE ACCESSING AND CONTROL
        711/167
                        .Access timing
        711/169
                        .. Memory access pipelining
  711/200
                  (0 \text{ OR}, 2 \text{ XR})
                 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
        Class
                          SYSTEMS: MEMORY
                        ADDRESS FORMATION
        711/200
                  (1 OR, 1 XR)
2 711/203
                 711:
                         ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
        Class
                          SYSTEMS: MEMORY
                                         Page 4
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711/200 ADDRESS FORMATION 711/202 Address mapping (e.g., conversion, translation) 711/203 ..Virtual addressing

10660310_CLS1.txt Most Frequently Occurring Classifications of Patents Returned From A Search of 10660310 on March 07, 2006

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Original Classifications

4 711/118

3 711/144

2 711/130
           711/135
           711/168
            711/3
Cross-Reference Classifications
           711/146
711/119
711/144
711/145
711/3
     4
            711/118
711/133
     ż
           711/133
712/23
709/213
710/105
710/22
711/125
711/138
     3
     2222222
            711/139
           711/139
711/140
711/143
711/147
711/156
711/165
711/169
711/200
711/207
     2
     2 2 2
Combined Classifications
7 711/118
7 711/144
6 711/146
6 711/3
5 711/145
     6
5
4
            711/119
            711/133
     4
     3
            710/22
           711/130
711/135
711/138
711/143
711/147
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            709/213
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710/108
710/52
711/100
711/113
     2
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            711/121
            711/122
            711/125
            711/139
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10660310_CLS1.txt

2 2 2	711/140 711/156 711/159
2	711/165
2	711/168
2	711/169
2	711/200
Ž	711/203

Page 2

10660310_EAST1.txt

PLUS Search Results for S/N 10660310, Searched March 07, 2006

The Patent Linguistics Utility System (PLUS) is a USPTO automated search system for U.S. Patents from 1971 to the present. PLUS is a query-by-example search system which produces a list of patents that are most closely related linguistically to the application searched. This search was prepared by the staff of the Scientific and Technical Information Center, SIRA.

EAST



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abstract 1
accordance 1
adapt 1
adaptive 3
algorithm 3
an 1
and 4
any 2
applicable 1
application 1
architecture 2
as 3
assistants 1
at 4
aware 1
back 1
background 1
based 6
battery 1
be 14
begins 1
between 1
bij 1
brian 1
brief 1
by 1
cache 9
caching 1 cameras 1
case 1
cases 1
cease 1
cellular 1
chart 1
complicate 1
computer 1
computers 1
conditions 1
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conventional 1
correctly 2
coulson 1
coupled 3
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data 2
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electrodes 1 electronic 2
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environments 2
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ferro 1
ferroelectric 5
figure 4
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freewamuste 1
from 1
function 2
generally 1
ĥе 1
higher 5
houston 1
however 1
hu 1
hub 4
if 1
in 9
include 2
including 1
input 2
invention 5
inventors 1
is 5
itl 2
its 1
katg 1
laptop 1
leete 1
letters 1
loss 1
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may 19
memories 6
memory 18
mobile 3
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operating 1
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or 3
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other 1
output 2
page 4
pair 1
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personal 2
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pruner 1
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rate 1
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relates 1
relies 1
rely 1
remainder 1
rest 1
richard 1
schematic 1
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september 1
shutdown 2
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slower 1
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subjected 1
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temperature 12
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the 24
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thus Ž
timothm 1
title 1
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transitioned 1
trop 2
turn 1
tx 1
types 1
unexpected 3
united 1
us 3
uses 1
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